Question lab 1 cpen 311:

Is it combinational or sequential?

Is signaltap just capture this? Can it capture longer stuffs?

Can I change line 341?

Which clock on line 289?

Does LED turns on at the beginning?

How to make LEDR have a value first and then use case?

Always combinational invoke when something in the combinational logic change?

Can line 341 ff combine with line 344?

logic direction, rst;

logic [7:0] NLED;

always\_ff @(posedge CLK\_1Hz)

begin

//if (rst)

//begin

LED <= 8'd1;

direction <= 1'd0;

rst <= 1'd0;

//end

//else

//LED <= NLED;

//if (({direction, LED} == {1'd0, 8'd64}) || ({direction, LED} == {1'd1, 8'd2}))

//direction <= ~direction;

end

always\_comb

begin

case (direction)

1'd0: NLED[7:0] = LED[7:0] << 1;

1'd1: NLED[7:0] = LED[7:0] >> 1;

default: NLED[7:0] = 8'dx;

endcase

end

this shit doesn’t work. Not sure why?

When use combinational and when use sequential?